## Form A2-3: Compliance Verification Report for Inverter Connected Power Generating Modules

This form should be used by the **Manufacturer** to demonstrate and declare compliance with the requirements of EREC G99/NI. The form can be used in a variety of ways as detailed below:

1. To obtain **Fully Type Tested** status

   The **Manufacturer** can use this form to obtain **Fully Type Tested** status for a **Power Generating Module** by registering this completed form with the Energy Networks Association (ENA) Type Test Verification Report Register.

2. To obtain **Type Tested** status for a product

   This form can be used by the **Manufacturer** to obtain **Type Tested** status for a product, which is used in a **Power Generating Module** by registering this form with the relevant parts completed with the Energy Networks Association (ENA) Type Test Verification Report Register.

3. One-off Installation

   This form can be used by the **Manufacturer** or **Installer** to confirm that the **Power Generating Module** has been tested to satisfy all or part of the requirements of this EREC G99/NI. This form must be submitted to the **DNO** as part of the application.

A combination of (2) and (3) can be used as required, together with Form A2-4 where compliance of the **Interface Protection** is to be demonstrated on site.

Note:

Within this Form A2-3 the term **Power Park Module** will be used but its meaning can be interpreted within Form A2-3 to mean **Power Park Module**, **Generating Unit or Inverter** as appropriate for the context. However, note that compliance must be demonstrated at the **Power Park Module** level.

If the **Power Generating Module** is **Fully Type Tested** and registered with the Energy Networks Association (ENA) Type Test Verification Report Register, the Installation Document (Form A3-1 or A3-2) should include the **Manufacturer's** reference number (the Product ID), and this form does not need to be submitted.

Where the **Power Generating Module** is not registered with the ENA Type Test Verification Report Register or is not **Fully Type Tested** this form (all or in parts as applicable) needs to be completed and provided to the **DNO**, to confirm that the **Power Generating Module** has been tested to satisfy all or part of the requirements of this EREC G99/NI.

| | | | |
|---|---|---|---|
| **Manufacturer's** reference number | | ERD-CR202109003 | |
| **PGM** technology | | S5-EH1P3K-L,S5-EH1P3.6K-L,S5-EH1P4.6K-L,S5-EH1P5K-L,S5-EH1P6K-L, | |
| **Manufacturer** name | | Ginlong Technologies Co., Ltd. | |
| Address | | No. 57 Jintong Road, Seafront (Binhai) Industrial Park, Xiangshan, Ningbo, Zhejiang,315712,P.R.China | |
| Tel | (+86) 574 6580 3377 | Web site | www.ginlong.com |
| E:mail | Ruyi.pan@ginlong.com | | |
| **Registered Capacity** | | 3kVA/3.6kVA/4.6kVA/5.0kVA,6.0kVA | |

There are four options for Testing: (1) **Fully Type Tested**, (2) Partially **Type Tested**, (3) one-off installation, (4) tested on site at time of commissioning. The check box below indicates which tests in this Form have been completed for each of the options. With the exception of **Fully Type Tested PGMs** tests marked with * may be carried out at the time of commissioning (Form A4).

| **Tested option:** | **1. Fully Type Tested** | **2.Partially Type Tested** | **3. One-off Man. Info.** | **4. Tested on Site at time of Commission-ing** |
|---|---|---|---|---|
| 0. **Fully Type Tested**- all tests detailed below completed and evidence attached to this submission | **Yes** | **N/A** | **N/A** | **N/A** |
| 1. Operating Range | **N/A** | -- | -- | -- |
| 2. PQ – Harmonics | | -- | -- | -- |
| 3. PQ – Voltage Fluctuation and Flicker | | -- | -- | -- |
| 4. PQ – DC Injection (**Power Park Modules** only) | | -- | -- | -- |
| 5. **Power Factor** (PF)* | | -- | -- | -- |
| 6. Frequency protection trip and ride through tests* | | -- | -- | -- |
| 7. Voltage protection trip and ride through tests* | | -- | -- | -- |
| 8. Protection – Loss of Mains Test*, Vector Shift and RoCoF Stability Test* | | -- | -- | -- |
| 9.**LFSM-O** Test* | | -- | -- | -- |
| 10. Protection – Reconnection Timer* | | -- | -- | -- |
| 11. Fault Level Contribution | | -- | -- | -- |
| 12. Self-monitoring Solid State Switch | | -- | -- | -- |
| 13. Wiring functional tests if required by para 15.2.1 (attach relevant schedule of tests)* | | -- | -- | -- |
| 14. Logic Interface (input port)* | | -- | -- | -- |

* may be carried out at the time of commissioning (Form A.2-4).

Document reference(s) for **Manufacturers' Information:**

**Manufacturer** compliance declaration. - I certify that all products supplied by the company with the above **Type Tested Manufacturer's** reference number will be manufactured and tested to ensure that they perform as stated in this document, prior to shipment to site and that no site **Modifications** are required to ensure that the product meets all the requirements of EREC G99/NI.

| | | | |
|---|---|---|---|
| Signed | 17.Oct.2021<br>Caojiaqi | On behalf of<br>Manufacturer stamp | 锦浪科技股份有限公司<br>GINLONG TECHNOLOGIES CO.,LTD.<br>Ginlong Technologies Co., Ltd. |

Note that testing can be done by the **Manufacturer** of an individual component or by an external test house.

Where parts of the testing are carried out by persons or organisations other than the **Manufacturer** then that person or organisation shall keep copies of all test records and results supplied to them to verify that the testing has been carried out by people with sufficient technical competency to carry out the tests.

| **A2-3 Compliance Verification Report –Tests for Type A Inverter Connected Power Generating Modules – test record** | |
|---|---|
| **1. Operating Range:** Two tests should be carried with the **Power Generating Module** operating at **Registered Capacity** and connected to a suitable test supply or grid simulation set. The power supplied by the primary source shall be kept stable within ± 5 % of the apparent power value set for the entire duration of each test sequence.<br>Frequency, voltage and **Active Power** measurements at the output terminals of the **Power Generating Module** shall be recorded every second. The tests will verify that the **Power Generating Module** can operate within the required ranges for the specified period of time.<br>The **Interface Protection** shall be disabled during the tests.<br>In case of a PV **Power Park Module** the PV primary source may be replaced by a DC source.<br>In case of a full converter **Power Park Module** (e.g. wind) the primary source and the prime mover **Inverter**/rectifier may be replaced by a DC source. | |
| Test 1<br>Voltage = 85% of nominal (195.5 V),<br>Frequency = 47.5 Hz,<br>**Power Factor** = 1,<br>Period of test 90 minutes | Tested with the specified conditions, in the 90 minutes period of time, the inverters operate normally |
| Test 2<br>Voltage = 110% of nominal (253 V).,<br>Frequency = 51.5 Hz,<br>**Power Factor** = 1,<br>Period of test 90 minutes | Tested with the specified conditions, in the 90 minutes period of time, the inverters operate normally |
| Test 3<br>Voltage = 110% of nominal (253 V),<br>Frequency = 52.0 Hz,<br>**Power Factor** = 1,<br>Period of test 15 minutes | Tested with the specified conditions, in the 15 minutes period of time, the inverters operate normally |

**2. Power Quality – Harmonics**:

For **Power Generating Modules** of **Registered Capacity** of less than 75 A per phase (i.e. 50 kW) the test requirements are specified in Annex A.7.1.5.These tests should be carried out as specified in BS EN 61000-3-12The results need to comply with the limits of Table2 of BS EN 61000-3-12 for single phase equipment and Table 3 of BS EN 610000-3-12 for three phase equipment.
**Power Generating Modules** with emissions close to the limits laid down in BS EN 61000-3-12 may require the installation of a transformer between 2 and 4 times the rating of the **Power Generating Module** in order to accept the connection to a **Distribution Network.**
For **Power Generating Modules** of **Registered Capacity** of greater than 75 A per phase (i.e. 50 kW) the installation must be designed in accordance with EREC G5.

**Power Generating Module** tested to BS EN 61000-3-12

| **Power Generating Module** rating per phase (rpp) | | | 6 | kVA | Harmonic % = Measured Value (A) x 23/rating per phase (kVA) | |
|---|---|---|---|---|---|---|
| Harmonic | At 45-55% of **Registered Capacity** | | 100% of **Registered Capacity** | | Limit in BS EN 61000-3-12 | |
| | Measured Value MV in Amps | % | Measured Value MV in Amps | % | 1 phase | 3 phase |
| 2 | 0.091 | 0.349 | 0.160 | 0.613 | 8% | 8% |
| 3 | 0.118 | 0.452 | 0.210 | 0.805 | 21.6% | Not stated |
| 4 | 0.024 | 0.092 | 0.042 | 0.161 | 4% | 4% |
| 5 | 0.095 | 0.364 | 0.187 | 0.716 | 10.7% | 10.7% |
| 6 | 0.015 | 0.057 | 0.030 | 0.115 | 2.67% | 2.67% |
| 7 | 0.087 | 0.333 | 0.139 | 0.532 | 7.2% | 7.2% |
| 8 | 0.011 | 0.042 | 0.022 | 0.084 | 2% | 2% |
| 9 | 0.067 | 0.257 | 0.108 | 0.414 | 3.8% | Not stated |
| 10 | 0.009 | 0.034 | 0.016 | 0.061 | 1.6% | 1.6% |
| 11 | 0.065 | 0.249 | 0.105 | 0.403 | 3.1% | 3.1% |
| 12 | 0.007 | 0.027 | 0.015 | 0.058 | 1.33% | 1.33% |
| 13 | 0.060 | 0.230 | 0.098 | 0.376 | 2% | 2% |
| THD[1] | --- | 1.46 | --- | 1.53 | 23% | 13% |
| PWHD[2] | --- | 2.85 | --- | 3.47 | 23% | 22% |

[1] THD = Total Harmonic Distortion

[2]PWHD = Partial Weighted Harmonic Distortion

**3. Power Quality – Voltage fluctuations and Flicker**:

For **Power Generating Modules** of **Registered Capacity** of less than 75 A per phase (i.e. 50 kW) these tests should be undertaken in accordance with Annex A.7.1.4.3. Results should be normalised to a standard source impedance, or if this results in figures above the limits set in BS EN 61000-3-11 to a suitable Maximum Impedance.

For **Power Generating Modules** of **Registered Capacity** of greater than 75 A per phase (i.e. 50 kW) the installation must be designed in accordance with EREC P28.

| | Starting | | | Stopping | | | Running | |
|---|---|---|---|---|---|---|---|---|
| | d max | d c | d(t) | d max | d c | d(t) | P st | P lt 2 hours |
| Measured Values at test impedance | 0.30% | 0.22% | 0 | 0.30% | 0.23% | 0 | 0.10 | 0.10 |
| Normalised to standard impedance | 0.30% | 0.22% | 0 | 0.30% | 0.23% | 0 | 0.10 | 0.10 |
| Normalised to required maximum impedance | N/A | N/A | N/A | N/A | N/A | N/A | N/A | N/A |
| Limits set under BS EN 61000-3-11 | 4% | 3.3% | 3.3% | 4% | 3.3% | 3.3% | 1.0 | 0.65 |

| | | | | | | |
|---|---|---|---|---|---|---|
| Test Impedance | R | 0.24 *<br>0.4 ^ | Ω | XI | 0.15 *<br>0.25 ^ | Ω |
| Standard Impedance | R | 0.24 *<br>0.4 ^ | Ω | XI | 0.15 *<br>0.25 ^ | Ω |
| Maximum Impedance | R | N/A | Ω | XI | N/A | Ω |

* Applies to three phase and split single phase **Power Generating Modules.**

^ Applies to single phase **Power Generating Module** and **Power Generating Modules** using two phases on a three phase system

For voltage change and flicker measurements the following formula is to be used to convert the measured values to the normalised values where the **Power Factor** of the generation output is 0.98 or above.

Normalised value = Measured value x reference source resistance/measured source resistance at test point

Single phase units reference source resistance is 0.4 Ω

Two phase units in a three phase system reference source resistance is 0.4 Ω

Two phase units in a split phase system reference source resistance is 0.24 Ω

| | | | |
|---|---|---|---|
| Three phase units reference source resistance is 0.24 Ω<br>Where the **Power Factor** of the output is under 0.98 then the XI to R ratio of the test impedance should be close to that of the Standard Impedance.<br>The stopping test should be a trip from full load operation.<br>The duration of these tests need to comply with the particular requirements set out in the testing notes for the technology under test. Dates and location of the test need to be noted below | | | |
| Test start date | 11.Oct.2021 | Test end date | 12.Oct.2021 |
| Test location | Ginlong Technologies Co.,Ltd. | | |
| **4. Power quality – DC injection:** The tests should be carried out on a single **Generating Unit**. Tests are to be carried out at three defined power levels ±5%. At 230 V a 6 kW single phase **Inverter** has a current output of 26.1 A so DC limit is 65.2 mA. These tests should be undertaken in accordance with Annex A.7.1.4.4. | | | |
| Test power level | 10% | 55% | 100% |
| Recorded value in Amps(mA) | 33.2 | 42.4 | 46.1 |
| as % of rated AC current | 0.127 | 0.162 | 0.177 |
| Limit | 0.25% | 0.25% | 0.25% |
| **5. Power Factor**: The tests should be carried out on a single **Power Generating Module**. Tests are to be carried out at three voltage levels and at **Registered Capacity**. Voltage to be maintained within ±1.5% of the stated level during the test. These tests should be undertaken in accordance with Annex A.7.1.4.2. | | | |
| Voltage | 0.94 pu (216.2V) | 1 pu (230V) | 1.1 pu (253V) |
| Measured value | 0.9988 | 0.9989 | 0.9991 |
| **Power Factor** Limit –leading | >0.95 | >0.95 | >0.95 |
| **Power Factor** Limit –leading | >0.98 | >0.98 | >0.98 |

**6. Protection – Frequency tests:** These tests should be carried out in accordance with the Annex A.7.1.2.3.

| Function | Setting | | Trip test | | "No trip tests" | |
|---|---|---|---|---|---|---|
| | Frequency | Time delay | Frequency | Time delay | Frequency /time | Confirm no trip |
| U/F | 48Hz | 0.5s | 47.98Hz | 0.514s | 48.2Hz 25s | Yes |
| | | | | | 47.8Hz 0.45s | Yes |
| O/F | 52Hz | 1.0s | 52.02Hz | 1.029s | 51.8Hz 120s | Yes |
| | | | | | 52.2Hz 0.98s | Yes |

Note. For frequency trip tests the frequency required to trip is the setting ± 0.1 Hz. In order to measure the time delay a larger deviation than the minimum required to operate the projection can be used. The "No trip tests" need to be carried out at the setting ± 0.2 Hz and for the relevant times as shown in the table above to ensure that the protection will not trip in error.

**7. Protection – Voltage tests:** These tests should be carried out in accordance with Annex A.7.1.2.2.

| Function | Setting | | Trip test | | "No trip tests" | |
|---|---|---|---|---|---|---|
| | Voltage | Time delay | Voltage | Time delay | Voltage /time | Confirm no trip |
| U/V stage 1 | 0.85pu (195.5V) | 3.0s | 195.2V | 3.020s | 199.5V 5s | Yes |
| U/V stage 2 | 0.6pu (138V) | 2s | 138.3V | 2.025s | 142V 2.5s | Yes |
| | | | | | 134V 1.98s | Yes |
| O/V | 1.1pu (253V) | 0.5s | 253.1V | 0.524s | 249V 5.0s | Yes |
| | | | | | 257V 0.45s | Yes |

Note for Voltage tests the Voltage required to trip is the setting ±3.45 V. The time delay can be measured at a larger deviation than the minimum required to operate the protection. The No trip tests need to be carried out at the setting ±4 V and for the relevant times as shown in the table above to ensure that the protection will not trip in error.

**8.Protection – Loss of Mains test:** These tests should be carried out in accordance with BS EN 62116.

| Annex A.7.1.2.4. | | | | | | |
|---|---|---|---|---|---|---|
| The following sub set of tests should be recorded in the following table. | | | | | | |
| Test Power and imbalance | 33% -5% Q Test 22 | 66% -5% Q Test 12 | 100% -5% P Test 5 | 33% +5% Q Test 31 | 66% +5% Q Test 21 | 100% +5% P Test 10 |
| Trip time. Limit is 0.5s | 0.30s | 0.29s | 0.24s | 0.32s | 0.30s | 0.27s |

**Loss of Mains Protection, Vector Shift Stability test.** This test should be carried out in accordance with Annex A.7.1.2.6.

| | Start Frequency | Change | Confirm no trip |
|---|---|---|---|
| Positive Vector Shift | 49.5Hz | +50 degrees | Yes |
| Negative Vector Shift | 50.5Hz | - 50 degrees | Yes |

**Loss of Mains Protection, RoCoF Stability test:** This test should be carried out in accordance with Annex A.7.1.2.6.

| Ramp range | Test frequency ramp: | Test Duration | Confirm no trip |
|---|---|---|---|
| 49.0Hz to 51.0Hz | $+0.95Hzs^{-1}$ | 2.1s | Yes |
| 51.0Hz to 49.0Hz | $-0.95Hzs^{-1}$ | 2.1s | Yes |

| **9. Limited Frequency Sensitive Mode – Over frequency test:** The test is using the specific threshold frequency of 50.2 Hz and Droop of 4%.<br>This test should be carried out in accordance with Annex A.7.1.3. | |
|---|---|
| **Active Power** response to rising frequency/time plots are attached if frequency injection tests are undertaken in accordance with Annex A.7.2.4. | **Yes** |
| Alternatively, simulation results should be noted below: | |

| Test sequence at **Registered Capacity**>80% | Measured **Active Power** Output | Frequency | Primary Power Source | **Active Power** Gradient |
|---|---|---|---|---|
| Step a) 50.00Hz ±0.01Hz | 6012W | 50.00Hz | 6275W | - |
| Step b) 50.25Hz ±0.05Hz | 5865W | 50.25Hz | | - |
| Step c) 50.70Hz ±0.10Hz | 4520W | 50.70Hz | | - |
| Step d) 51.15Hz ±0.05Hz | 3194W | 51.15Hz | | - |
| Step e) 50.70Hz ±0.10Hz | 4524W | 50.70Hz | | - |
| Step f) 50.25Hz ±0.05Hz | 5877W | 50.25Hz | | - |
| Step g) 50.00Hz ±0.01Hz | 5991W | 50.00Hz | | 36kW/min |
| Test sequence at **Registered Capacity** 40% - 60% | Measured **Active Power** Output | Frequency | Primary Power Source | **Active Power** Gradient |
| Step a) 50.00Hz ±0.01Hz | 3022W | 50.00Hz | 3178W | - |
| Step b) 50.25Hz ±0.05Hz | 2880W | 50.25Hz | | - |
| Step c) 50.70Hz ±0.10Hz | 1535W | 50.70Hz | | - |
| Step d) 51.15Hz ±0.05Hz | 211W | 51.15Hz | | - |
| Step e) 50.70Hz ±0.10Hz | 1536W | 50.70Hz | | - |
| Step f) 50.25Hz ±0.05Hz | 2878W | 50.25Hz | | - |
| Step g) 50.00Hz ±0.01Hz | 3015W | 50.00Hz | | 36kW/min |

| **10. Protection – Re-connection timer.** | | |
|---|---|---|
| Test should prove that the reconnection sequence starts after a minimum delay of 60 s for restoration of voltage and frequency to within the stage 1 settings of Table 10.1. | | |
| Time delay setting | Measured delay | Checks on no reconnection when voltage or frequency is brought to just outside stage 1 limits of Table 10.1. |

| | | | | | |
|---|---|---|---|---|---|
| 70s | 73s | At 257.0V | At 191.5V | At 47.9Hz | At 52.1Hz |
| Confirmation that the **Power Generating Module** does not re-connect. | | Yes | Yes | Yes | Yes |

**11. Fault level contribution**: These tests shall be carried out in accordance with EREC G99/NI Annex A.7.1.5.

For **Inverter** output

| Time after fault | Volts | Amps |
|---|---|---|
| 20ms | 52.2V | 29.8A |
| 100ms | 51.7V | 0A |
| 250ms | 51.3V | 0A |
| 500ms | 51.3V | 0A |
| Time to trip | 0.062s | In seconds |

**12. Self-Monitoring solid state switching:** No specified test requirements. Refer to Annex A.7.1.7.

| | |
|---|---|
| It has been verified that in the event of the solid state switching device failing to disconnect the **Power Park Module**, the voltage on the output side of the switching device is reduced to a value below 50 volts within 0.5 s. | N/A (Solid state switch means electronic switch, Solis inverter uses mechanical dual relay protection with relay checks, which drops the voltage below 50V in 0.5s) |

**13. Wiring functional tests:** If required by para 15.2.1.

| | |
|---|---|
| Confirm that the relevant test schedule is attached (tests to be undertaken at time of commissioning) | N/A(Not applicable. Refer to 15.2.1, inverter is using special connector for wiring) |

**14. Logic interface (input port).**

| | |
|---|---|
| Confirm that an input port is provided and can be used to shut down the module. | Yes (Logic interface is marked as "DRM" either on inverter or on external DRM device depending on inverter model. Please see inverter or external DRM device manual for detail.) |

Additional comments.

The test result is based on S5-EH1P6K-L. All the series of inverters electrical character are the same. So the test result can cover all series.